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Inventor(s): Allan Chertok, J. Thomas Fowler, Darrell J. King,
Gerald R. Larocque, Per Onnerud and Jay J. Shi
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SYSTEM AND METHOD FOR BALANCING STATE OF CHARGE
AMONG SERIES-CONNECTED ELECTRICAL ENERGY STORAGE UNITS

RELATED APPLICATIONS

This application claims the benefit of U.S. Provisional Application No. 60/464,391, filed April 18, 2003 and U.S. Provisional Application No. 60/428,666, filed November 25, 2002. The entire teachings of the above applications are incorporated
5 herein by reference.

BACKGROUND OF THE INVENTION

Hybrid electric vehicles (HEVs) combine the internal combustion engine of a conventional vehicle with the battery and electric motor of an electric vehicle. This
10 combination offers the driving range and rapid refueling features to which consumers are accustomed with conventional vehicles, while achieving improved fuel economy and lower emissions.

Typical HEV designs such as those already on the market and those which will be introduced shortly, are so-called "parallel" configurations. In the parallel HEV, the
15 battery powered motor is principally used to boost engine torque for hill climbing and high acceleration demands. When boost torque is not required, the engine drives the electric motor as a generator to recharge the battery. The motor is also driven as a generator during braking events, thus, relieving thermal loading of the conventional

friction brakes and enabling recovery of vehicle kinetic and potential energy which is returned to the battery.

The battery "pack" of a typical HEV consists of one or more "modules" of series-connected "cells." Nickel cadmium (NiCad) and nickel metal hydride (NiMH) cells have been successfully employed in recently introduced HEVs while higher performance lithium ion (Li-ion) cells are envisioned for future generation designs.

Desirable attributes of battery cells for HEV applications are high-peak specific power, high specific energy at pulse power, fast charge acceptance to maximize regenerative braking utilization, and long calendar and cycle life. Achieving a favorable HEV battery pack lifetime requires some means to monitor cell state of charge (SOC) and control of cell charging and discharging to assure that all cells in the pack are well "balanced" or "equalized," for example, at a nominally uniform state of charge. The development of means to achieve affordable and reliable balanced cell operation, especially for newer Li-ion cells, has presented significant technical challenges

Lithium ion batteries are now widely used in laptop computer and cell-phone products because of their high specific energy. They also have high specific power, high energy efficiency, good high-temperature performance, and low self-discharge. Components of lithium ion batteries could also be recycled. These characteristics make lithium ion batteries desirable for HEV applications. However, to make them commercially viable for HEVs, further development is needed to improve calendar and cycle life and cost.

SUMMARY OF THE INVENTION

If lithium-ion batteries are to be successfully employed in HEV applications, the state of charge of individual battery cells will need to be continuously balanced to maintain a high cell calendar life and cell capacity. Cells must have their state of charge equalized toward a target state of charge so they uniformly support HEV operation. Furthermore, care must be taken to assure that an individual cell is not charged beyond its safe limit. State of charge may be determined from an open circuit cell voltage

measurement, or under load, from a measurement of cell voltage combined with cell impedance and current.

Despite the performance advantages of lithium-ion battery technology, there is a cost tradeoff associated with increased complexity of the controls required to equalize the battery state of charge. The achievement of an affordable solution is particularly challenging in the case of very long high voltage series strings of cells required for an HEV.

According to one aspect of the invention, a system and method are provided for balancing state of charge among plural series connected electrical energy storage units. Cell chemistries other than lithium-ion, such as nickel-cadmium, lead-acid and nickel metal hydride, may also benefit from embodiments of this cell balancing system and method.

According to one embodiment, the system can include a string of electrical energy storage units where each storage unit has a state of charge. A circuit selectively monitors the state of charge of each storage unit. When the state of charge of a selected unit is greater than a target state of charge, the circuit transfers energy from the selected unit to the string of storage units, such that the state of charge of the selected unit converges toward the target state of charge. When the state of charge of a selected unit is less than the target state of charge, the circuit transfers energy from the string of storage units to the selected unit, such that the state of charge of the selected unit converges toward the target state of charge.

In particular embodiments, the circuit can include a power converter that transfers energy from the selected unit to charge the string of units when the state of charge of the selected unit is greater than the target state of charge. Conversely, when the state of charge of the selected unit is less than the target state of charge, the power converter transfers energy from the string of units to charge the selected unit. The power converter can include an up converter that transfers energy from the selected unit to charge the string of units and a down converter that transfers energy from the string of units to charge the selected unit. The power converter can also be a common

transformer that is used as both a down converter to charge the selected unit and an up converter to discharge the selected unit depending on the state of charge of the selected unit.

In particular embodiments, the circuit can include semiconductor switches that
5 selectively couple the power converter to the selected storage unit. The semiconductor switches can be bidirectional or unidirectional.

With bidirectional switches, the same pair of switches can electrically couple the power converter to charge or discharge the selected storage unit. A polarity selector can also be implemented to connect the pair of bidirectional switches to the power
10 converter.

With unidirectional switches, different pairs of switches are used to electrically coupling the power converter to charge and discharge the selected storage unit. For example, when the state of charge of the selected unit is greater than the target state of charge, a first pair of the unidirectional switches electrically couple the power converter
15 to the selected storage unit for discharging. Conversely, when the state of charge of the selected unit is less than the target state of charge, a second pair of unidirectional switches electrically couple the power converter to the selected storage unit for charging.

In particular embodiments, the circuit can include a controller that selectively
20 monitors the state of charge of each storage unit and directs the power converter to transfer energy between the selected unit and the string of units such that the state of charge of the selected unit converges towards the target state of charge. To monitor the state of charge of a selected storage unit, the controller can utilize voltage and current data of the selected unit that is detected by a first sensor in the circuit. To determine the
25 target state of charge, the controller can utilize the total current data for the string of storage units that is detected by a second sensor in the circuit.

According to another embodiment, the system may include plural strings of storage units that are coupled in series with each storage unit having a state of charge. Corresponding circuits selectively monitor the state of charge of each storage unit in a

corresponding string of units. For each string of units, when the state of charge of a selected unit is different than a target state of charge, the corresponding circuit transfers energy between the selected unit and the corresponding string of units, such that the state of charge of the selected unit converges towards the target state of charge.

5 In particular embodiments, the system can include a master controller that determines a corresponding target state of charge for each string of units. The master controller then directs each of the corresponding circuits in transferring energy between a selected unit and the corresponding string of units such that the state of charge of the selected unit converges towards the corresponding target state of charge.

10 Each of the corresponding circuits can include a power converter which transfers energy from the selected unit to charge the corresponding string of units when the state of charge of a selected unit is greater than the corresponding target state of charge. Conversely, the power converter transfers energy from the corresponding string of units to charge the selected unit when the state of charge of the selected unit is less than the
15 corresponding state of charge.

 In another particular embodiment, the master controller can determine a common target state of charge and can direct each of the corresponding circuits in transferring energy between a selected unit and the corresponding string of units such that the state of charge of the selected unit converges towards the common target state
20 of charge.

 According to another embodiment, the system can include a string of electrical energy storage units with each storage unit having a state of charge and a circuit that selectively monitors the state of charge of each storage unit. When the state of charge of a selected unit is greater than a target state of charge, the circuit transfers energy from
25 the selected unit to a non-dissipative load, such that the state of charge of the selected unit converges towards the target state of charge. When the state of charge of the selected unit is less than the target state of charge, the circuit transfers energy from the non-dissipative load to the selected unit, such that the state of charge of the selected unit converges toward the target state of charge.

According to another embodiment, the system can include a string of electrical energy storage units where each storage unit has a state of charge. A circuit selectively monitors the state of charge of each storage unit under a load. When the state of charge of a selected unit is different than a target state of charge, the circuit transfers energy
5 between the selected unit and the string of storage units, such that the state of charge of the selected unit converges toward the target state of charge.

Each storage unit can be a storage cell or a battery module having a string of cells. A battery pack includes a string of one or more storage units. Furthermore, when the string of units are in a battery module, a battery pack can be formed of multiple
10 battery modules.

BRIEF DESCRIPTION OF THE DRAWINGS

The foregoing and other objects, features and advantages of the invention will be apparent from the following more particular description of preferred embodiments of the invention, as illustrated in the accompanying drawings in which like reference
15 characters refer to the same parts throughout the different views. The drawings are not necessarily to scale, emphasis instead being placed upon illustrating the principles of the invention.

Fig. 1 is a diagram illustrating a cell balancing system according to one embodiment.

20 Fig. 2 is a more detailed diagram illustrating a cell balancing system according to one embodiment.

Fig. 3A and 3B are conceptual diagrams for illustrating the bidirectional charging operation of the equalization converter according to one embodiment.

Fig. 4A and 4B are schematic diagrams for illustrating the operation of the
25 switch matrix having unidirectional switches.

Fig. 5 is a more detailed diagram illustrating a cell balancing system according to an alternative embodiment.

Fig. 6A and 6B are schematic diagrams for illustrating the operation of the switch matrix having bidirectional switches according to one embodiment.

Fig. 7A-7D are flow charts illustrating a method of operating the cell balancing system according to one embodiment.

5 Fig. 8A is a schematic diagram illustrating the operation of a boost converter according to one embodiment.

Fig. 8B is a schematic diagram illustrating the operation of a buck converter according to one embodiment.

10 Fig. 9 is a diagram of a cell balancing system having reduced switch blocking requirements according to one embodiment.

Figs. 10 and 11 illustrate particular embodiments for reducing switch voltage blocking requirements utilizing the unidirectional and bidirectional switch matrix configurations respectively.

15 Fig. 12 is a diagram illustrating a cell balancing system according to an alternative embodiment.

DETAILED DESCRIPTION OF THE INVENTION

A description of preferred embodiments of the invention follows.

20 Fig. 1 is a diagram illustrating a cell balancing system according to one embodiment. The cell balancing system 1 includes a battery pack 100 that further includes a string of series-connected storage cells 105 for storing energy. The battery pack 100 serves as a source to power, for example, a Hybrid Electric Vehicle (HEV) motor 200. The battery pack 100 may also receive charge from the HEV motor 200 when it is driven as a generator either by the HEV engine or by vehicle kinetic and potential energy during braking operations.

25 The battery pack 100 is further coupled to a switch matrix 300, such that each of the storage cells 105 in the pack 100 may be individually selected and electrically coupled to an equalization converter 400 for monitoring of voltage, current, and temperature and for equalization of the individual cell state of charge toward a target

state of charge. In particular embodiments, the switch matrix 300 may be an assembly of semiconductor switches, facilitating integrated circuit design and reduced fabrication costs.

A control unit 500 directs the switch matrix 300 in selectively coupling
5 individual cells to the equalization converter 400 and monitors the state of charge of the individual cells 105 through communication with the equalization converter 400.

According to one embodiment, the control unit 500 is a micro-controller with on-chip analog-to-digital converter (ADC) of modest resolution (e.g., 10 bits) and a conversion rate of, for example, 1000 per second. When the state of charge of an individual cell is
10 different than a target state of charge, the control unit 500 directs the equalization converter 400 to transfer energy between the individual cell and the string of cells 100, such that the state of charge of the selected cell converges toward a target state of charge.

For example, when the state of charge of a selected cell is greater than the target,
15 the cell is incrementally discharged by directing the equalization converter 400 to draw current from the cell and to return the charge back to the battery pack 100 via connection 401. Conversely, when the state of charge of the selected cell is less than the target, the cell is charged by directing the equalization converter 400 to draw current from the battery pack 100 via connection 401 and to deliver it to the selected cell. State
20 of charge may be determined from a voltage measurement under a fixed load using a method depicted in Figs. 7A-7D and subsequently described. In such embodiments, an individual cell may be charged or discharged in a non-dissipative manner.

According to alternative embodiments, the current drawn (*i.e.*, discharged) from the selected cell may be transferred to another non-dissipative load. A load is non-
25 dissipative if at least 80% of the energy through that element is conserved. A non-dissipative load can include an intermediate energy storage buffer (*e.g.*, a capacitor or an auxiliary power supply) for subsequent charging of the string of cells or an individual cell. According to further alternative embodiments, the non-dissipative load may also

include another storage cell. Additional switches and equalization converters may need to be incorporated for transferring energy to the other storage cell.

State of charge (SOC) is a measure that characterizes electrochemical states of a cell after a charging or discharging process. For example, assume that the full capacity of a cell is 'z' Ah (Ampere-hour). After discharging 0.5z Ah capacity from the fully charged state, the state of charge of the cell is 50%. Likewise, after discharging 0.25z Ah capacity from the fully charged state, the state of charge is 25%, and so on.

The state of charge of a cell can be correlated to cell open circuit voltages through electrochemical titration of the cell. As an approximation, a cell's state of charge is often correlated to cell voltages through charging or discharging voltage profiles at very low charging or discharging rates. Since cell state of charge reflect the electrochemical state, it is important to equalize a cell's state of charge in order to enhance cell life and cell safety.

According to particular embodiments of the invention, the state of charge is measured by measurement of voltage, current, and impedance. The state of charge is equalized by correcting for the voltage difference that is due to the load and then comparing voltages of individual cells.

In prior art techniques, charge is measured and used as an indicator of cell equalization. However, charge, which is measured in Ampere-hour (Ah), is quite different from state of charge (SOC). It is not sufficient to only measure charge in order to equalize cells, because cells having the same charge may not have the same state of charge.

For example, assume two cells have a capacity of 20 Ah. When the cells are charged to 20Ah, both have a 4.2V potential. Assuming that full cell capacity can be utilized, the cells can be equalized to a state of charge of 50% by equalizing the charge of the cells to 10Ah, resulting in a certain voltage less than 4.2V.

However, assume further that the two cells degrade differently, such that the first cell can only achieve a maximum capacity of 18Ah and the second cell can only achieve a maximum capacity of 15Ah in their respective degraded state. When both cells are

charged to their maximum capacities, both have a voltage potential of 4.2V. In this case, if the two cells are equalized to an equilibrium charge of 10Ah, the first cell would be discharged by 8Ah and the second cell would be discharged by 5Ah. This results in the cells having different states of charge (i.e., 56% and 67% respectively), and thus two
5 quite different cell voltages. Hence the cells are not considered equalized.

Therefore, the method of equalizing cell charge does not become reliable for the degraded cells or for cells that are not equivalent in nature. This inability to equalize state of charge leads to low battery life and safety hazards. The low life is caused by potential deep discharge or overcharging, resulting from the inability to determine state
10 of charge. Further, and especially for lithium-ion batteries, the overcharge or over-discharge may cause thermal runaway due to possible formation of dendrites that can cause internal shortage of the cells.

According to particular embodiments of the invention, the impedance of a cell, and therefore the state of charge, can preferably be measured by employing a test current
15 coming from or into the cell and measuring the voltage in response. The cell resistance is determined by dividing the measured cell voltage by the measured cell current. Additional information about the more complex cell impedance can be determined by measuring the resistance change over time in response to a constant current or a current profile.

20 The test current can be a controlled current that can be programmed by the controller to take a current sample from or supply a current sample to the cell in a non-dissipative manner via the converter. The test current can also employ the actual load current going into or out of the string of cells as the vehicle brakes or accelerates.

In the case of the controlled current, the amplitude and duration of the current
25 pulse can be tailored to optimize the impedance measurement. In the case of a load current due to vehicle motion, the measurement of impedance may not be optimized, but it can occur during normal vehicle operation.

Both types of test current impedance measurement may be combined if necessary. For example, if the vehicle is drawing only a light load from the stack that is

not sufficient to accurately measure the impedance of a particular cell, the converter may be programmed to draw just enough extra current from the cell to provide an accurate measurement.

For batteries, when determining internal impedance of the cell, the voltage and
5 current are preferably measured during a fast pulse of the system. This impedance is only due to the so-called internal impedance and not due to other polarization effects in the battery. This is also the impedance that is used to calculate the voltage that corresponds to a certain state of charge, by correcting for the load applied to the battery during operation.

10 Fig. 2 is a more detailed diagram illustrating a cell balancing system according to one embodiment. In this embodiment the switch matrix 300 includes an assembly of $2(N+1)$ unidirectional switches (not shown) where N is the number of cells in the battery pack 100. In order to electronically couple an individual cell to, for example, the equalization converter 400, the control unit 500 energizes a pair of unidirectional
15 switches that provides a conduction path between the converter 400 and an individual cell 105. In particular, a first selected pair of switches provides a conduction path for charging the cell and a second selected pair of switches provides a conduction path for monitoring and discharging the cell. Implementation of switch matrix 300 with unidirectional switches is depicted in Figs. 4A and 4B and subsequently described.

20 The equalization convertor 400 is used to transfer energy between an individual cell and the string of cells 100 when the state of charge for the selected cell is different than the target state of charge. The control unit 500 provides control signals directing the converter 400 to transfer energy into a selected cell from the string of cells 100 or to transfer energy out of the cell back into the string of cells 100. The control unit 500 also
25 provides control signals for limiting current to and from the cell and for identifying a target state of charge (e.g., cell voltage).

The equalization converter 400 includes a buck converter 412, also referred to as a down converter. When the state of charge of an individual cell is less than a target

state of charge, the control unit 500 directs the buck converter 412 to transfer energy from the string of cells to charge the individual cell.

According to one embodiment, the buck converter 412 may be a galvanically isolated buck converter circuit, which can accept charge from a pack of series-connected cells or a battery module of cells (e.g., a 400-volt battery pack of five 80-volt modules) and deliver it to a selected cell. The buck converter input voltage range can be matched to the maximum voltage produced by a string of cells in a battery module (e.g., 80 volts) or the entire battery pack itself (e.g., 400 volts). The buck converter output can be matched to the voltage of an individual cell. The converter output may also have its voltage and current limits programmable by the control unit 500.

The buck converter power rating may be determined by the maximum equalization charge current demand. For example, the peak power rating of a buck converter providing a maximum equalization charge current of 10 Amperes delivered at 4 Volts is 40 Watts, resulting in the size and cost of the circuit components being relatively small. The buck converter may also supply a small amount of control power necessary to operate cell monitoring and equalization control circuits.

In one particular embodiment, the buck converter is a Beta Dyne 40W Power Watt™ DC/DC Converter. In another particular embodiment, the buck converter is implemented as a combined buck/boost converter, which can be enabled to perform either function. A boost converter returns charge to the pack. The operation of a buck converter according to one embodiment is illustrated in Fig. 8B.

The equalization converter 400 further includes a boost equalization converter 414, also referred to as an up converter. When the state of charge of the individual cell is higher than the target state of charge, the control unit 500 directs the boost converter 414 to transfer energy from the selected cell to charge the string of cells 100.

According to one embodiment, the boost converter 414 may be a galvanically isolated boost converter circuit which can accept charge from a selected cell and return it to a pack of cells or a module of cells (e.g., a 400 volt battery pack employing five 80-volt modules). Its output provides a current source with adequate voltage to assure

charge acceptance at the maximum desired rate over the operating voltage range of the pack or module to which it is connected.

The boost converter may have its output voltage limited to avoid over-voltage when disconnected. The boost converter may have its input voltage and current limits
5 determined by the control unit 500. For example, the input may be configured to accept charge from the connected cell over the expected range of cell voltages. The size and cost of the circuit components of the boost converter may be similar to the buck converter. The operation of a boost converter according to one embodiment is illustrated in Fig. 8A.

10 The cell balancing system further includes a cell voltage and current monitor 420, and a current sensor 430. The cell voltage and current monitor 420 reports selected cell voltage and current to the control unit 500 for determining the state of charge of that cell. The cell voltage and current monitor 420 also observes battery current sensor 430. Cell voltage and current data is utilized by the control unit 500 for determining a target
15 state of charge dynamically. When each of the individual states of charge of the cells converges toward the target state of charge, the charge will be equalized across the string of cells, facilitating longer operating and calendar life.

Fig. 3A and 3B are conceptual diagrams for illustrating the bidirectional charging operation of the equalization converter according to one embodiment. At this
20 conceptual level, attention is focused on the galvanic isolation provided by transformer coupling means (a,b) employed in the buck converter 412 and boost converter 414. Switch mode power supply circuitry required to interface the DC input and DC outputs with the transformer is assumed but not shown.

In Fig. 3A, when the buck converter 412 is directed by the control unit 500 to
25 transfer energy into selected cell V3, current from the string of cells 100 is drawn through an inductor element "b," causing induced current to be driven from the buck converter 412 into selected cell V3 via inductor element "a." For example, in Fig. 3A, where there are 5 storage cells in the string, the voltage across the string of cells must be down converted to about the target voltage of the individual cell, or about 1/5 the string

voltage. Assuming all cells have about the same impedance and 1 A of induced current is driven into the selected cell from the buck converter 412, about $1/5$ A will be drawn from the string. The resultant current through the cell being charged will then be approximately $(1\text{A} - 1/5\text{A}) = 4/5$ A of current for charging the selected cell, while $1/5$ A of current is discharged from the remaining cells.

Conversely, in Fig. 3B, when the boost converter 414 is directed by the control unit 500 to transfer energy out of the selected cell V3, current from the selected cell is drawn through an inductor element "a," causing induced current to be driven from the boost converter 414 back into the string of cells 100 via inductor element "b." For example, in Fig. 3B, where there are 5 storage cells in the string and 1 A of induced current is drawn from the selected cell by the equalization discharge converter 414, $4/5$ A of current (i.e., $1\text{A} - 1/5\text{A} = 4/5\text{A}$) is discharged from the selected cell, while $1/5$ A of current is delivered to the remaining cells for charging.

Fig. 4A and 4B are schematic diagrams for illustrating the operation of the switch matrix having unidirectional switches. The operation of the switch matrix permits a cell to be selectively charged or discharged and its voltage to be monitored. For embodiments of the cell balancing system, such as illustrated in Fig. 2, an assembly of N cells requires $2(N+1)$ independently controlled unidirectional switches S.

Although not so limited, in the illustrated embodiment the unidirectional switches are MOSFET switches S with each coupled to a blocking diode D. The unidirectional switches are arranged such that by energizing a pair of switches, a conduction path through the selected cell is maintained for monitoring and/or transferring energy in or out of the cell. Since switches S are unidirectional, one pair of switches may be enabled to selectively charge an individual cell and another pair of switches may be enabled to discharge or monitor the same cell.

For example, in Fig. 4A, it is possible to selectively charge an individual cell V1 by enabling unidirectional MOSFET switches S9 and S12. Current taken from a battery of cells via the buck converter 412, which is represented by controlled current source I2, flows from switch S9 and diode D9 through cell V1 and returns back to the switch

matrix 300 by switch S12 and diode D12. In this example, blocking diodes D10 and D11 prevent cell V1 from being short circuited through the intrinsic body diodes of S10 and S11 while S9 and S12 are conducting. For example, the intrinsic body diode bd7 of switch S7 is shown in Fig. 4A. That diode allows conduction from right to left even
5 when switch S7 is gated OFF. Such conduction would provide a short circuit from the positive node of V2, through the intrinsic body diode of S7 and the ON switch S9 if the diode D7 were not in place.

Similarly, in Fig. 4B, it is possible to discharge an individual cell V1 by enabling unidirectional MOSFET switches S11 and S10. Charge returning to the battery of cells
10 via the boost converter 414, which is represented by a controlled current sink I1, flows from switch S11 and diode D11 through cell V1 and returns back to the switch matrix 300 by switch S10 and diode D10. Alternatively, I1 may be a current limited voltage sink (i.e. one which permits cell discharge to a controllable potential).

As noted above, a diode in series with each unidirectional MOSFET switch is
15 required to block undesired conduction paths. To provide minimal voltage drop across the blocking diodes, Schottky diodes may be used. Because the associated current is known, it is possible to compensate for the blocking voltage drop in charge, discharge, and measurement modes of operation. The residual non-systematic voltage drop after compensation is due to variations in diode properties among devices and cannot be
20 compensated, but it is small relative to the operating voltages. This combined charge/discharge capability with a single controlled matrix provides the ability to transfer charge selectively among cells to provide for cell balancing.

According to particular embodiments, the unidirectional switches may also be insulated gate bipolar transistors (IGBT). However, MOSFET technology avoids the
25 conduction loss associated with the IGBT saturation voltage drop ($V_{ce\ sat}$). MOSFET switches provide low impedance when turned ON. In particular, the losses due to drop across the MOSFET "on-resistance" ($R_{DS\ on}$) are relatively small and there is low MOSFET "on-resistance" variation among devices and with temperature variation, providing an acceptable level of error in all voltage measurements through the matrix.

Isolated gate drives of the MOSFET switches may facilitate implementation in the form of an application specific integrated circuit (ASIC). According to one embodiment, the gate drivers are galvanically isolated light emitting diode (LED) driven photovoltaic (PV) gate drivers. The modest turn-ON/turn-OFF speeds achieved by these drivers are more than adequate for the intended application.

Fig. 5 is a more detailed diagram illustrating a cell balancing system according to an alternative embodiment. This embodiment is substantially similar in operation to that of Fig. 2 with the exception of the configuration and operation of switch matrix 300 and the additional polarity selector 440 that is controlled by control unit 500. The polarity selector 440 is coupled between the switch matrix 300 and the equalization converter 400.

In particular, the switch matrix 300 includes a bank of $N+1$ bidirectional switches (not shown) where N is the number of cells in the battery pack 100. In order to electronically couple an individual cell to, for example, the equalization converter 400, the control unit 500 energizes a pair of bidirectional switches that provides a conduction path between the converter 400 and the cell. One pair of switches may be used to selectively charge or discharge an individual cell.

The control unit 500 determines whether to charge or discharge the cell based on whether the state of charge of the selected cell is greater or less than a target state of charge. When the state of charge is greater, the control unit 500 sends a signal to the equalization converter 400 to discharge the cell. At the same time, the control unit 500 also sends a control message to the polarity selector 440 to select the polarity of links 445 and 447 which are coupled to the switch matrix 300, as discussed in Fig. 6A. The polarity selector may be implemented with MOSFET switches and galvanically isolated LED-PV gate drivers or electromechanical relays. Other implementations of the polarity selector known to those skilled in the art may suffice.

Fig. 6A and 6B are schematic diagrams for illustrating the operation of the switch matrix having bidirectional switches according to one embodiment. The operation of the switch matrix permits a cell to be selectively charged or discharged and

its voltage to be monitored. Note that for this embodiment, a pair of unidirectional MOSFETs connected "back-to-back" comprises a single bidirectional switch. This will be explained in detail in Fig. 6B.

For embodiments of the cell balancing system, such as illustrated in Fig. 5, an assembly of N cells requires (N+1) independently controlled bidirectional switches. Thus, the number of switching components is reduced from implementations that utilize unidirectional switches. This reduction also corresponds to a reduction in the number of gate drivers that are required. The bidirectional switches are arranged such that by energizing a pair of switches, a conduction path through the selected cell is maintained for monitoring and/or transferring energy in or out of the cell.

Since switches are bidirectional, a single pair of switches may be enabled to both charge or discharge an individual cell. For example, in Fig. 6A it is possible to selectively charge cell V5 by enabling switch M1/M2 and switch M3/M4. To charge the cell, the equalization converter 400 causes current to flow in a direction from switch M1/M2, charging cell V5 and exiting through switch M3/M4 back to the cell balancing circuitry. In particular, the internal resistance of the equalization circuit 400 is represented by a resistor and the string of cells 100 is represented by a battery symbol.

Similarly, in order to selectively charge cell V4, switches M5/M6 and M3/M4 must be enabled. However, in order to drive current from the equalization converter 400 into cell V4 for charging, the polarity across the pair of switches must be reversed. The polarity is set by the control unit 500 sending control signals to the polarity selector 440 to set the polarity across links 445 and 447. The selection of polarity is based, for example, on the particular arrangement of the bidirectional switches coupling to the individual cells. Other arrangements of bidirectional switches may be implemented avoiding the need for the polarity selector.

It is also possible to selectively discharge cell V5 by enabling the same bidirectional switches M1/M2 and M3/M4 that were enabled to charge the cell. To discharge the cell, the equalization converter 400 causes current to flow in an opposite direction from M3/M4, discharging cell V5, and exiting through switch M1/M2 back to

the cell balancing circuitry. The same polarity set for charging may be used for discharging.

However, in order to selectively discharge cell V4, switches M5/M6 and M3/M4 are enabled and the polarity across the pair of switches must be reversed. Thus, the
5 equalization converter 400 is able to withdraw current from cell V4, such that current flows from switch M5/M6, discharging cell V4 and exiting through switch M3/M4 back to the cell balancing circuitry.

Fig. 6B is a schematic diagram of a bidirectional switch according to one embodiment. In this embodiment, the bidirectional switch 305 is a pair of discrete
10 MOSFET switches (M1, M2) having a common gate 305g and source 305s. To turn on the bidirectional switch, a voltage is applied to the gate 305g. According to one embodiment, the gate is driven by galvanically isolated LED photovoltaic (PV) gate drivers. Current flows in a direction determined by the equalization converter 400 and the polarity applied at terminals d1 and d2 as applied by links 445 and 447 of the
15 polarity selector 440.

With this configuration, a forward biased intrinsic body diode, bd1 or bd2, does not introduce a voltage drop to compromise cell voltage measurement accuracy because current flows principally through the channel of the associated MOSFET device and the voltage drop across its channel resistance is much lower than a diode drop, and thus
20 dominant. Moreover, when the bidirectional switch gate drive is off (i.e., zero or close to zero volts), the MOSFET switch pair blocks conduction in both directions thus avoiding need for the blocking diodes required for the unidirectional switch embodiment depicted in Fig. 4A and Fig. 4B. Thus, the illustrated embodiment of the switch provides low voltage drop switching with lower power dissipation and high
25 accuracy in cell voltage and state of charge estimation, due to lower contaminating voltage drops in the circuit.

According to particular embodiments, the bidirectional switches may be implemented with insulated gate bipolar transistor (IGBT) devices or electromechanical relays, or Semiconductor Assisted Relay Switches. IGBT switches do not have an

intrinsic reverse diode and provide only unidirectional conduction. Hence, if parallel "back-to-back" IGBT switches are utilized to implement the bidirectional switch previously described, each IGBT switch must contain series blocking diodes to prevent reverse current.

- 5 While bidirectional IGBT switches are applied in power switching applications, the conducting IGBT and reverse diode introduce contaminating voltage drops which are highly undesired here. MOSFET switches provide low impedance when turned ON, and thus are generally preferred for this reason. However, where voltage and current rating requirements cannot be affordably met by MOSFET switches, IGBT
- 10 embodiments may be viable provided that compensation for the IGBT and diode conduction voltage drops is provided.

 If electromechanical relays can attain suitable cycle life and their size and weight are not objectionable, they too may be employed for implementing the cell selection switch matrix in lieu of MOSFET bidirectional switches, with the advantage of avoiding

15 the need for LED-PV drive isolation and virtual elimination of conduction losses. According to still further embodiments, the bidirectional switches may also be implemented with parallel-connected "back-to-back" silicon controlled rectifiers (SCRs).

 Isolated gate drives of the MOSFET switches and the MOSFET switches

20 themselves may facilitate implementation in the form of an application specific integrated circuit (ASIC). Moreover, more than one isolated gate drive bidirectional switch might be advantageously integrated in one ASIC chip. According to one embodiment, the gate drivers are galvanically isolated LED driven photovoltaic (PV) gate drivers. The modest turn ON, turn OFF speeds are more than adequate for the

25 intended application.

 Fig. 7A-7D are flow charts illustrating a method of operating the cell balancing system according to one embodiment. Although not so limited, this method may be implemented as an algorithm that causes the control unit 500 to perform the following procedure. Referring to Fig. 7A, the process starts at 1000. At 1020, the status of the

motor is checked. If the motor is off, the method returns to 1000 and waits to poll the motor status again. If the motor is started, a cell number n is initialized to 1 at 1040 to select the first cell of the battery pack 100. At 1060, the process calls subroutine "READ IMPEDANCE."

5 Referring to Fig. 7B at 2000, subroutine "READ IMPEDANCE" is started. At 2020 a pair of switches is energized, connecting the selected cell (i.e., cell $n=1$). At 2040, the open circuit voltage (V_{open}) of the selected cell is read. At 2060, a load is applied to the selected cell, causing current to flow from the selected cell and the cell voltage to drop. The applied load may be the boost converter 414. By using the boost
10 converter 414, energy from the cell is not lost, as compared with a dissipative load. Rather, the boost converter 414 recharges the battery 100 with the current discharged from the cell.

At 2080 the loaded voltage (V_{load}) and current (I_{cell}) of the selected cell are read. At 2100 the impedance of the selected cell is calculated and stored in variable
15 R_{n2} . The cell impedance R_{n2} is calculated as a ratio of the voltage difference between the loaded voltage (V_{load}) and the open circuit voltage (V_{open}) divided by the cell current (I_{cell}). The previously saved cell impedance value is stored in variable R_{n1} .

Since cell impedance can vary due to the dynamic usage of the battery pack, the steps 2060 and 2080 may be repeated multiple times in order to obtain a more accurate
20 determination of cell impedance. For example, according to one embodiment, the boost converter may be operated such that constant amplitude current pulses (i.e., DC pulses) are withdrawn from the cell at fixed or varied time intervals, as discussed in reference to Fig. 8A. Whenever a DC pulse is drawn from the cell, the loaded voltage may be measured, resulting in a number of loaded voltage measurements. Whenever a DC
25 pulse is not being drawn from the cell, the open circuit voltage across the cell may be measured, resulting in a corresponding number of open circuit voltage measurements. The voltage difference between the open circuit voltage and the voltage under load is referred to as a voltage ripple. From the voltage ripples and corresponding current

pulses obtained over a period of time, a more accurate cell impedance may be inferred. The resulting cell impedance value may then be stored in variable Rn2.

According to a further embodiment, the applied load may be a buck converter operated such that constant amplitude current pulses (*i.e.*, DC pulses) can be inserted
5 into the cell at fixed or varied time intervals to infer cell impedance in a similar manner.

At 2120 the applied load is removed. At 2140 the change in cell impedance (ΔR) between the present and previous cell impedance values (Rn2, Rn1) is calculated. At 2160 it is determined whether the present value Rn2 has changed by 50% or more from the previous value Rn1. If so, a RETRY command is issued at 2180 causing the
10 process to loop back to 2040 to recalculate the present cell impedance Rn2. If not, the previous impedance value Rn1 is replaced with the present value Rn2 at 2200. At 2220 the selected pair of switches is de-energized, decoupling the selected cell from the cell balancing circuitry. At 2240, subroutine "READ IMPEDANCE" returns.

Referring back to Fig. 7A at 1080, the cell number 'n' is compared with the total
15 number of cells N in the pack to determine whether all of the present cell impedances have been determined. If not, the cell number 'n' is incremented at 1100 and subroutine "READ IMPEDANCE" is repeated until all of the present cell impedances R1, . . . RN have been determined.

At 1120 the sum of the present cell impedance values ΣR is calculated. At 1140
20 the pack voltage, V_p , and pack current, I_p are read. The pack current is measured by current sensor 430 and saved. At 1160 a setpoint voltage V_{set} is calculated by averaging the pack voltage V_p , offset by the product of the pack current I_p and the sum of the cell impedances ΣR , over the total number of cells N. In particular, the pack current I_p has a positive value when pack current enters the pack. Conversely, the pack
25 current I_p has a negative value when pack current exits the pack. The set point voltage V_{set} is referred to as the target state of charge. At 1180, subroutine "SCAN CELLS" is called.

Referring to Fig. 7C, at 3000 subroutine "SCAN CELLS" is started. At 3020 the cell number 'n' is initialized to 1 to select the first cell in the pack. At 3040, a load is

applied to the circuit. The applied load may be the boost converter 414. At 3060 a pair of switches is energized, connecting the selected cell to boost converter 414. Current flows from the selected cell and the cell voltage drops. At 3080 the loaded voltage V_n and cell current I_n are read. At 3100 the state of charge of the selected cell is calculated.

- 5 According to one embodiment, the state of charge V_{cell} is the difference between the loaded voltage V_n and the product of the cell current I_n and the present cell impedance R_n . In particular, where the applied load (e.g., boost converter) draws current out of the cell, the cell current I_n has a negative value. Conversely, where the applied load (e.g., buck converter) draws current into the cell, the cell current I_n has a
- 10 positive value. At 3120, the state of charge V_{cell} is stored in variable V_n .

- At 3140 the selected pair of switches is de-energized, decoupling the selected cell from the boost converter 414. At 3160 the cell number 'n' is compared with the total number of cells N in the pack to determine whether the state of charge V_{cell} has been calculated for each cell in the pack. If not, the cell number 'n' is incremented at
- 15 3180 and a next pair of switches is energized to electrically couple the next selected cell. This is repeated until all the individual cells have been analyzed. At 3200, subroutine "SCAN CELLS" returns.

- Referring back to Fig. 7A, at 1200 the individual cells are prioritized for equalization based on the difference between their state of charge V_{cell} and the target
- 20 state of charge V_{set} . According to one embodiment, the individual cells are sorted for equalization with the individual cell having the largest difference being selected first. Additionally, the amounts of time for charging a cell or returning charge to the pack may be proportioned according to the amount of this difference. According to another embodiment, the individual cells are accessed sequentially for equalization. At 1220
- 25 subroutine "EQUALIZE" is called.

Referring to Fig. 7D, at 4000 subroutine "EQUALIZE" is started. At 4060 a pair of switches is energized, connecting the selected cell with the highest priority. At 4080 a load is applied to the selected cell, causing current to flow from the selected cell and the cell voltage to drop. The applied load may be the boost converter 414. At 4000 the

loaded cell voltage V_n and cell current I_n are read. At 4120 the state of charge V_{cell} of the selected cell is calculated. According to one embodiment, the state of charge V_{cell} is the difference between the loaded voltage V_n and the product of the cell current I_n and the present cell impedance R_n . In particular, where the applied load (e.g., boost
5 converter) draws current out of the cell, the cell current I_n has a negative value.

Conversely, where the applied load (e.g., buck converter) draws current into the cell, the cell current I_n has a positive value. After calculating the state of charge V_{cell} , the applied load is removed at 4140. At 4160 the state of charge V_{cell} of the selected cell is compared with the target state of charge V_{set} .

10 If the state of charge V_{cell} is less than target state of charge V_{set} , the buck converter 412 is applied to the selected cell at 4180. At 4200 the charge time is monitored as determined by prioritization, and the applied voltage is monitored at 4220. If the charge time expires, the equalization charger circuit is removed at 4340. If the charge time has not expired, but the voltage from the buck converter that is applied to
15 the selected cell is too high, the charging voltage is decreased at 4240.

If the state of charge (V_{cell}) is greater than target state of charge (V_{set}), the boost converter 414 is applied to the selected cell at 4260. At 4280 the discharge time is monitored as determined by prioritization, and the applied voltage is monitored at 4300. If the discharge time expires, the boost converter 414 is removed at 4340. If the
20 discharge time has not expired, but the voltage being applied from the selected cell to the input of the boost converter is too low, the discharge current is decreased at 4240.

If the state of charge V_{cell} is equal to target state of charge V_{set} , within a pre-established deadband, the equalization converter 400 is removed from the selected cell at 4340. At 4360 the selected pair of switches is de-energized, decoupling the
25 selected cell from the cell balancing circuitry. At 4380 subroutine "EQUALIZE" returns.

Referring back to Fig. 7A, at 1240 the equalize time is compared with a pre-determined interval (e.g. 5 minutes). If the equalize time does not exceed the pre-determined interval, the routine loops back to 1180 to repeat subroutine "SCAN

CELLS" in preparation for another equalization. If the pre-determined interval has been exceeded, the temperatures of the individual cells are scanned at 1260. At 1280 each of the scanned temperatures T is compared to a pre-established high temperature limit, T_{lim} . If any of the scanned temperatures T is greater than temperature limit T_{lim} , the system is shut down at 1320. If not, a determination is made at 1300 whether the temperature of any cell has changed ($|\Delta T|$) by more than a prescribed increment T_c (e.g., 5 degrees Celsius). If not, the routine loops back to 1180 to repeat subroutine "SCAN CELLS" in preparation for another equalization. If so, the routine loops back to 1040, resetting the cell number to 1 and repeating the process at subroutine "READ IMPEDANCE".

Embodiments of the equalization converter 400 may include separate or integrated buck and boost power converters to monitor the condition of an individual, selectable, cell in a series string of cells. Such embodiments also achieve desired cell equalization, such as charging an individual cell from the series string (buck mode) or transferring excess energy from an overcharged cell to the series string (boost mode). In particular, charge may be added or subtracted from an individual cell in a non-dissipative manner.

With respect to monitoring in general, testing of cell state of charge involves measuring temperature, open circuit voltage, and voltage under a fixed load for a certain period of time. Typically, the load current used is a discharge current into a resistive load, resulting in wasted energy (i.e., dissipation). If a conventional dissipative load is applied to a cell, it is normal to keep the measurement time as short as possible to avoid discharging the cell.

Embodiments of the equalization converter can be used in a cell balancing system to make the necessary measurements of open circuit voltage and voltage under load, as well as the change of these voltages over time, in a non-dissipative manner by monitoring the amplitude of the voltage ripple across the cell while transferring energy in either a positive (charge) or negative (discharge) mode without wasting energy. This

non-dissipative technique has the advantage of extending this measurement time indefinitely, since it is occurring as part of the normal cell balancing process.

In particular, the equalization converter withdraws or inserts constant amplitude current pulses into a cell. In the time interval between these pulses, the open circuit
5 voltage is present and measured. At the peak of the current pulses, the internal resistance of the cell affects the voltage in a positive or negative way depending on the direction of the current. The difference between the open circuit voltage and the voltage under load is the ripple. From multiple determinations of the voltage ripple, a more accurate cell impedance can be inferred. The length of time these current pulses are
10 applied is the charge or discharge time.

In addition, low resistance bi-directional cell selector switches that connect the equalization converter to an individual cell are essentially lossless connections for monitoring the ripple across the cell.

With respect to equalization of cell state of charge, the equalization converter
15 400 removes energy in integer "packets" from an individual cell in a series string of storage cells (*e.g.*, lithium ion cells) and transfers that energy to the series string as a whole or vice versa. The rate of transfer of these packets represents the power being transferred. A timer produces a packet by charging the primary side of an inductor / transformer for a period of time T_1 . A second timer controls the number of these
20 packets produced per second at a rate of $1/T_2$. Immediately following the period T_1 , the energy is transferred to the battery string via the discharge of the energy stored in the inductor / transformer. The primary and secondary of the converter are electrically isolated so that the primary can be switched to any arbitrary cell. The size of the energy packet is controlled by varying T_1 , and the power by varying the time T_2 .

25 Fig. 8A is a schematic diagram illustrating the operation of a boost converter according to one embodiment. In particular, the boost converter enhances monitoring of the state of charge within a cell by withdrawing fixed amplitude current pulses from the cell and observing the resultant voltage "ripple." The amplitude of the resulting ripple voltage can be used to infer the impedance of the cell. This technique provides a more

accurate indication of state of charge than the average cell voltage taken alone. The current pulses are "up-converted" to a higher voltage and returned to the series string with only negligible losses. This approach is an improvement over prior art equalization methods that place a dissipative load on selected cells, thus wasting their
 5 energy.

In more detail, a pulse generator T1 generates pulses of fixed length T1, that are used to close switch S1 connecting the cell to the inductor Lp. Current from the cell then charges the inductor (Lp) with a triangular shaped current ramp reaching a peak current (Ipp) of:

$$10 \quad I_{pp} = V_{cell} * L_p / T_1$$

The resulting pulse of energy (Ep) is stored in the magnetic field of the inductor. The pulse energy is: $E_p = \frac{1}{2} L_p * I_{pp}^2$. When pulse T1 transitions low, switch S1 is turned off and the magnetic field collapses. This causes the energy to discharge into the transformer secondary Ls. The peak current in the secondary winding (Ips), assuming
 15 negligible losses, is:

$$I_{ps} = (2 * V_{bat} / L_s)^{1/2}$$

This secondary peak current discharges into the output capacitor C1 in time T3, where:

$$T_3 = (V_{bat} * L_s) / I_{ps}$$

It follows that the power output is proportional to the number of T1 pulses per
 20 second, or 1/T2. When the secondary voltage across inductor Ls2 reaches a voltage determined by the voltage reference (Ref), the pulse generator T2 stops generating T1 pulses since the Enable signal is removed from the pulse generator T2. Ideally, the second secondary has the same number of turns as the first secondary Ls and therefore reflects the secondary voltage across the isolation barrier.

25 Fig. 8B is a schematic diagram illustrating the operation of a buck converter according to one embodiment. In particular, current is drawn from the battery stack 100 and delivered to an individually selected cell 105. By adding switch S2 on the secondary side and diode D1 on the primary side of the circuit in Fig. 8A completes the conversion to a buck converter. Not shown in Fig. 8B is isolation circuitry to drive

switch S2 across the isolation barrier. Additionally, minor adjustments to the voltage regulator and timer portions may also be necessary.

As shown, a pulse generator T1 generates pulses of fixed length T1 that are used to close switch S2 connecting the string of cells to the inductor Ls. Current from the
5 string then charges the inductor (Ls) with a triangular shaped current ramp reaching a peak current (Isp). The resulting pulse of energy (Ep) is stored in the magnetic field of the inductor Ls.

When pulse T1 transitions low, switch S1 is turned off and the magnetic field collapses. This causes the energy to discharge into the inductor Lp. This secondary
10 peak current discharges into the output capacitor C1 in time T3. It follows that the power output is proportional to the number of T1 pulses per second, or $1/T2$.

When the voltage across inductor Ls2 reaches a voltage determined by the voltage reference (Ref), the pulse generator T2 stops generating T1 pulses since the Enable signal is removed from the pulse generator T2. Ideally, inductor Ls2 has the
15 same number of turns as inductor Ls and therefore reflects the voltage across the isolation barrier.

Both diodes D1 and the secondary rectifier diode can be considered part of the switches S1 and S2, as they are the bulk substrate "intrinsic" diodes of N-channel FETS. These intrinsic diodes are now available in their companion FETS as high speed diodes
20 and improve the switching efficiency of the circuit. If the Boost circuit is built with an N channel FET as the rectifier on the secondary side, then no additional power components are needed to convert it to a buck circuit with associated savings in space and parts cost.

Fig. 9 is a diagram of a cell balancing system having reduced switch blocking
25 voltage requirements according to one embodiment. Due to the modular nature of embodiments of the cell balancing circuitry, the maximum voltage accommodated by the switch matrix switching devices may be limited to attainable and affordable ratings by connecting multiple cell balancing circuits in series, providing one for each module of cells constituting the HEV battery pack.

In this embodiment, multiple battery module units, or modules, 100a, 100b form the battery pack 100 having a total voltage potential V_p . Each battery module is coupled to a respective equalization converter 400a, 400b by a corresponding switch matrix 300a, 300b. The equalization converters 400a, 400b are coupled in series, such
5 that each converter is coupled across the terminals of a corresponding battery module 100a, 100b. This configuration reduces the switch blocking voltage requirement to V_p/M where M is the number of battery modules connected in series. In the illustrated embodiment, there are $M=2$ modules, resulting in each unit balancing the state of charge of a string of cells of the respective battery module 100a, 100b with the switch blocking
10 voltage requirement reduced in half. By reducing the switch blocking voltage, cost benefits exist due to the ability to use lower voltage rated switch components.

A master control unit 600 directs local control units 500a, 500b in selectively monitoring and equalizing (i.e. charging and discharging) individual cells of the battery modules 100a, 100b as previously described. The master control unit 600 and the
15 control units 500a, 500b may communicate over a galvanically isolated serial data communication link. In particular, the master control unit 600 broadcasts pack current and equalization setpoint values (i.e., target state of charge) to the local control units 500a, 500b. Likewise, the local control units may communicate diagnostic or prognostic status information to the master control unit 600, such as voltage, current,
20 and temperature, from the modules.

Figs. 10 and 11 illustrate particular embodiments for reducing switch voltage blocking requirements utilizing the unidirectional and bidirectional switch matrix configurations respectively. Such embodiments result in the individual cells of a corresponding battery module being equalized to a target state of charge. However, the
25 target state of charge in one battery module 100a may be different from the target state of charge for another battery module 100b.

Due to the modular nature of the cell balancing circuitry, many different configurations may be employed. For example, it is possible to have a configuration where one or more battery modules in a battery pack have their states of charge being

balanced by embodiments of the cell balancing system, while other battery modules within the same battery pack are balanced using other known cell balancing systems. Furthermore, it is also possible to employ a nested control system where one or more battery modules in a battery pack have their states of charge individually balanced by
5 embodiments of the cell balancing system and a secondary control system for balancing the state of charge across the entire pack. The secondary control system may be an embodiment of the cell balancing system or alternatively a different cell balancing system. Thus, the cell balancing may be applied to electrical energy storage units which may be cells or modules of multiple cells.

10 Fig. 12 is a diagram illustrating a cell balancing system according to an alternative embodiment. In the illustrated embodiment, equalization converters 400a, 400b are coupled to the terminals of the entire battery pack of M battery modules, resulting in the individual cells of each battery module being equalized to a common target state of charge. However, this configuration requires a switch blocking voltage
15 requirement that is at least equal to the maximum voltage of the pack.

While this invention has been particularly shown and described with references to preferred embodiments thereof, it will be understood by those skilled in the art that various changes in form and details may be made therein without departing from the scope of the invention encompassed by the appended claims.